

ABSTRACT:

To provide a circuit arrangement for filtering and/or selecting single frequencies or frequency ranges, particularly of signals intended for at least an integrated circuit and/or signals generated by at least an integrated circuit, said circuit arrangement (100) comprising at least two electric resonant circuits (10; 20; 30)

- 5 - with at least an inductive element (12; 22; 32) and
- at least a capacitive element (14; 24; 34),

which, at a given low power supply voltage, provides a dynamic range complying with the current requirements, on the one hand, and has a maximal quality factor (Q), on the other hand, and can be realized in an inherently symmetrical arrangement and combined with a
10 differential or balanced circuit technique to be preferred in integrated techniques, it is proposed that the resonant circuits (10; 20; 30), particularly the inductive elements (12; 22; 32) are magnetically fixedly coupled to each other, and in that at least a part, preferably all resonant circuits (10; 20; 30) of the circuit arrangement (100) are arranged at or on the integrated circuit, particularly on only one metallization plate (40) of the integrated circuit,
15 having an essentially constant ohmic resistance.

Fig. 1

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